

IN THE CLAIMS:

Listing of claims:

1. (original) A method for manufacturing a semiconductor device comprising:
forming a first electrode above a semiconductor substrate;
forming a dielectric material on the first electrode;
forming a second electrode on the dielectric material;
wherein the forming the first electrode and the forming the second electrode are controlled so that at least one of the first electrode and the second electrode are formed from a material comprising titanium nitride containing oxygen atoms, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

2. (original) A method as in claim 1, comprising forming the first electrode from the material comprising titanium nitride containing oxygen atoms, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

3. (currently amended) A method as in claim 1, further comprising forming the dielectric material so that the first electrode is positioned between the dielectric material and the semiconductor substrate, and further comprising forming the second electrode from the material comprising titanium nitride containing oxygen atoms, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

4. (currently amended) A method for manufacturing a semiconductor device comprising:
forming an insulating layer above a semiconductor substrate;
forming a conducting region above the insulating layer, the conducting region selected from the group consisting of a gate electrode and an undercoat wiring;

forming a dielectric layer above the conducting region;
 forming a film ~~selected from~~ comprising a titanium nitride layer containing oxygen atoms within a range of from 5 to 25 atomic %;
 forming an electrode for a capacitive element above the dielectric layer by processing the film; and
 forming an out-going electrode connected to the electrode for the capacitive element.

5. (original) A method as in claim 4, further comprising wherein the out-going electrode extends above the electrode for the capacitive element.

6. (original) A method for manufacturing a semiconductor device comprising:
 forming a diffusion layer in a semiconductor substrate;
 forming an insulating layer over the diffusion layer;
 forming a first through-hole in the insulating layer located above the diffusion layer;
 forming a film above the insulating layer and in the through-hole, the film comprising a material selected from the group consisting of a TiN, titanium nitride containing oxygen atoms, and MoSi_x;
 forming an electrode for a capacitive element connected to the diffusion layer through the first through-hole by processing the film;
 forming a dielectric layer above the electrode for the capacitive element;
 forming a second electrode for the capacitive element above the dielectric layer;
 forming a second through-hole passing through the insulating layer above the diffusion layer; and
 forming an out-going electrode connected to the diffusion layer through the second through-hole.

7. (currently amended) A method of manufacturing a semiconductor device according to claim 4,6, wherein the film comprising layer of material ~~comprises~~ titanium nitride containing oxygen atoms ~~and the titanium nitride containing oxygen atoms~~ is formed by depositing a TiN layer by sputtering and injecting oxygen ions into the sputtered TiN layer.

8. (original) A method of manufacturing a semiconductor device according to claim 7, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

9. (currently amended) A method of manufacturing a semiconductor device according to claim ~~4,6~~, wherein the film comprising layer of material ~~comprises~~ titanium nitride containing oxygen atoms ~~and the titanium nitride containing oxygen atoms~~ is formed by depositing a TiN layer by sputtering and oxidizing the sputtered TiN layer.

10. (original) A method of manufacturing a semiconductor device according to claim 9, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

11. (currently amended) A method of manufacturing a semiconductor device according to claim ~~4,6~~, wherein the film comprising layer of material ~~comprises~~ titanium nitride containing oxygen atoms ~~and the titanium nitride layer containing oxygen atoms~~ is formed by sputtering with Ti as a target in an atmosphere comprising oxygen gas and nitrogen gas.

12. (original) A method of manufacturing a semiconductor device according to claim 11, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

13. (original) A method of manufacturing a semiconductor device according to claim 6, wherein the layer of material comprises titanium nitride containing oxygen atoms and the titanium nitride layer containing oxygen is formed by forming a Ti layer by sputtering and annealing the Ti layer in an atmosphere comprising oxygen gas and nitrogen gas.

14. (original) A method of manufacturing a semiconductor device according to claim 13, wherein the titanium nitride containing oxygen atoms includes an amount of oxygen atoms within a range of from 5 to 25 atomic %.

15. (currently amended) A method of manufacturing a semiconductor device according to claim ~~6,15~~, wherein the layer of material comprises MoSi_x and the MoSi_x layer is formed by sputtering an MoSi_x target.

16. (original) A method of manufacturing a semiconductor device as in claim 15, wherein the MoSi_x target has the same composition as the MoSi_x film.

17. (currently amended) A method of manufacturing a semiconductor device according to claim 4, further comprising forming at least one of a resistance element and a fuse element, wherein the electrode for the capacitive element ~~comprises~~ has the same composition as that of ~~that~~ the at least one of a resistance element and a fuse element.

18. (currently amended) A method of manufacturing a semiconductor device according to claim ~~4,31~~, further comprising forming the electrode for the capacitive element is ~~formed~~ simultaneously with ~~any~~ the one of a resistance element and a fuse element.

19. (original) A semiconductor device comprising:
 forming a first diffusion layer region in a semiconductor substrate;
 forming an first insulating layer region disposed over the first diffusion layer region;
 forming a first through-hole in the first insulating layer region;
 forming a lower electrode for a capacitive element connected to the first diffusion layer region through the first through-hole, the lower electrode including an upper surface comprising a material selected from the group consisting of TiN, titanium nitride containing oxygen atoms, and MoSi_x ;

forming a dielectric layer disposed in direct contact with the upper surface of the first electrode;

forming an upper electrode for the capacitive element above the dielectric layer;

forming a first out-going electrode connected to the first diffusion layer region.

forming a second diffusion layer region in the semiconductor substrate;

forming a second insulating layer region disposed over the second diffusion layer region;
forming a second through-hole passing through the second insulating layer region directly above the second diffusion layer region, the second through-hole being spaced a distance away from the first through-hole;

forming an element on the second insulating layer region that is spaced apart from the lower electrode and formed from an identical material as the lower electrode, the element being connected to the second diffusion layer region through the second through hole;

forming a second out-going electrode connected to the second diffusion layer region.
forming a third diffusion layer region spaced from the first and second diffusion layer regions, wherein the second diffusion layer region is located between the first and second diffusion layer regions;

forming a third insulating layer region disposed over the third diffusion layer region;
wherein the element is also formed on the third insulating layer region; and
forming a third through-hole extending through the third insulating layer region directly above the third diffusion layer region, the element being connected to the third diffusion layer region through the third through-hole; and

a third out-going electrode connected to the third diffusion layer region.

20. (original) A semiconductor device as in claim 19, comprising forming the lower electrode upper surface from a material selected from the group consisting of (1) MoSi_x where X is in the range of 1.7 to 3.3, and (2) titanium nitride containing oxygen atoms where the oxygen atoms are in the range of 5 to 25 atomic %.